DWG#: 5077T
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Contact AMI to verify revision

OPERATING MANUAL
FOR
MODEL 5753, 5754

CAPACITOR CHARGING POWER SUPPLY

Analog Modules, Inc.
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DWG#: 5077T
REV: E
FILE#: 5077te.doc
DATE: 000310
DCN#: 3027

TABLE OF CONTENTS

DWG#: 5077T
REV: E
FILE#: 5077te.doc
DATE: 000310
ECN#: 3027

MODEL 5753, 5754

SPECIAL PRECAUTIONS

CAUTION

- Read this manual carefully before attempting to install or operate the model 5753, 5754.
- This unit contains no user serviceable parts. Manufacturer's warranty is void if field serviced.
- Proper installation is necessary to limit access to lethal voltages.

MODEL 5753, 5754

Spezielle Vorsichtsmaβregein

Achtung

- Lesen Sie bitte dieses Handbuch genau, bevor Sie das Modell 5753, 5754 anschlieβen oder in Betrieb nehmen.

- Reparaturen dürfen nur von autorisiertem Servicepersonal vorgenommen werden. Bei unbefugtem Öffnen des Gerätes erlischt die Herstellergarantie.

- Achtung: Teile können Hochspannung führen !!
  Eine einwandfrele Installation ist notwendig um das unbeabsichtigte Berühren von tödlichen Hochspannungen unmöglich zu machen.

LABELS

Abbreviations:

A	amperes
AC	alternating current
EC	degrees Celsius
CW	Continuous wave
Hz	hertz
IEC	International Electrotechnical Commission
kHz	kilohertz
kW	kilowatts
kΩ	kiloohms
(L)	line conductor, single phase system
mA	milliamp
mm	millimeter
mV	millivolt
(N)	neutral conductor, single phase system
V	volts
VAC	alternating voltage
VDC	direct voltage
W/EC	watts per degree Celsius
Z	impedance
OEM	original equipment manufacturer
PFN	pulse forming network
W	watts
pk	peak
HV	high voltage
RTN	return
N/C	no connection
f/s	full scale
Ω	ohms

Symbols:


Protective Earth (ground) Terminal


Local signal reference

Alternating Current


Earth (ground)

Direct Current


Dangerous Voltage

SECTION 1

INTRODUCTION

1.0 INTRODUCTION

The 5750 series isolated switch-mode power modules use proprietary power conversion techniques to provide the highest power density of any power module currently on the market. All models are designed to meet the isolation and leakage current requirements for UL544 and IEC 601-1.

All supplies feature open circuit, short circuit, and thermal overload protection, as well as active power factor correction.

The Model 5753 is a capacitor-charging module designed to repeatedly charge energy storage capacitors for pulsed solid-state laser applications.

The Model 5754 is a capacitor charging module designed to charge energy storage capacitors to a specified voltage and to maintain this output level for switched variable pulsewidth solid-state laser applications.

SECTION 2

ENVIRONMENTAL DATA

2.0 ENVIRONMENTAL DATA

2.1 Non-operating (transport and storage)

Ambient Temperature:	-40EC to +70EC
Relative Humidity:	10% to 100%, non-condensing
Atmospheric Pressure:	500hPa to 1060hPa (7.25 to 15.37 psia)

2.2 Environmental Risk

The substances used in the product pose no known health or environmental risk associated with the disposal of the product at the end of their useful lives.

SECTION 3

SET-UP AND INTERFACE

3.0 SET-UP AND INTERFACE

3.1 Mechanical Considerations

Installation begins with mounting the module in a suitable enclosure which complies with the following criteria:

A. Enclosure must provide protection against possible human contact with live parts.

B. Enclosure must be adequately grounded to protective earth to ensure operator safety, or constructed entirely of a non-conductive material. In the latter case, all internal exposed metal parts must be grounded to protective earth.

3.2 Electrical Connections

Electrical connections are made in three groups; the power input, HV output, and control interface groups. These groups are terminated in three different connectors.

3.2.1 Power Input Group

The power-input connector is a 3-position terminal block.

IEC 601-1 requires that both line and neutral be fused. Therefore, 25A fuses should be installed in series with both the high and low sides of the mains.

The power-input requirements are: 198 B 253 VAC ∀ 10%, 1 ι, 50/60 Hz.

Input power requirements for the modules are typically 4300W for the 5754, and 3700W for the 5753.

3.2.2 HV Output Group

The HV output connector is a Fischer D105 series connector. The mating connector is a Fischer S105A001/10.7-S using RG-8 coaxial cable. The HV output cable is supplied as an AMI 1530-09 assembly.

The HV output center conductor should connect to the load capacitor bank. The HV RTN (shield) should connect to the flashlamp common star ground.

Typical output power for the modules is 3500W for the 5754, and 3000W for the 5753. Power output will decline for any charger that is not operated at its full output voltage. Please refer to the output power charts for reduction of output power with reduction of output voltage.

3.2.3 Control Interface Group

The Control Interface connector is a standard 15-pin D-sub connector. The part # is DB-15S.

The reference figures for each signal are schematic representations of the interface, and may be found on the 5753/5754 Interface Circuits pages.

The following standard (-1) connections are available:

PIN	Signal Name	Description
1	TEMPERATURE TEST POINT	Represents charger temperature as a DC voltage through 4.7kΩ of output impedance (reference figure 1). Refer to temperature test point data chart. Shutdown occurs at approximately 72EC.
2	PROGRAM VOLTAGE	0 to 10V control differential input (reference Figure 2). Two circuits connected in parallel.
3	N/C RESERVED	NO CONNECTION.
4	PRIMARY INHIBIT	3.5 to 30V input to inhibit charger. 10kΩ load impedance (reference Figure 3).
5	N/C RESERVED	NO CONNECTION.
6	+5V REFERENCE	5V reference with 100Ω source impedance. 10mA maximum current draw. Overload on this line could interfere with normal charger operation (reference Figure 5).
7	OVERTEMP FAULT INDICATOR	(16V maximum) Open collector output rated to 16V and capable of sinking up to 15mA (reference Figure 6). Fault indicated by low condition.
8	END OF CHARGE INDICATOR	Diode isolated output of 15.5V capable of sourcing up to 15mA. Charge complete indicated by high output signal (reference Figure 4).
9	PROGRAM RETURN	0 to 10V control differential input return (reference Figure 2).
10	SIGNAL RETURN	Used for low current signal output, and input returns.
11	N/C RESERVED	NO CONNECTION.
12	N/C RESERVED	NO CONNECTION.
13	N/C RESERVED	NO CONNECTION.
14	N/C RESERVED	NO CONNECTION.

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5723/5724/5753/5754 INTERFACE CIRCUITS
(STANDARD INTERFACE)

FIG. 1 TEMPERATURE TEST POINT

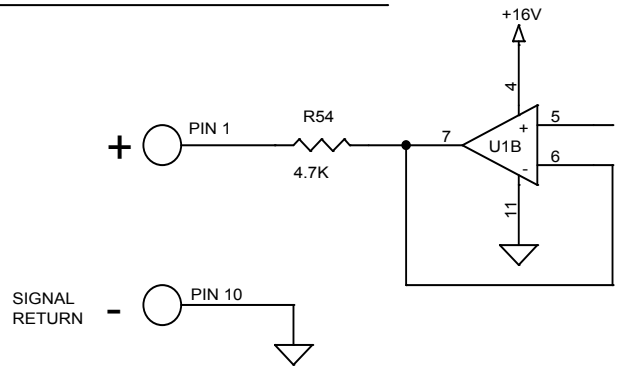
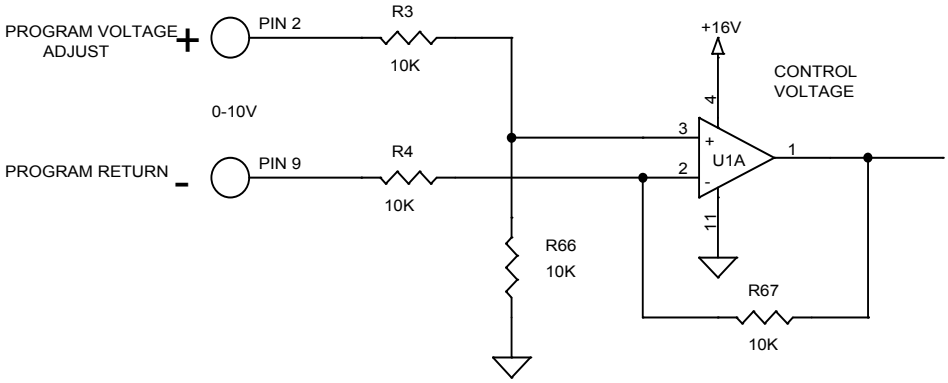


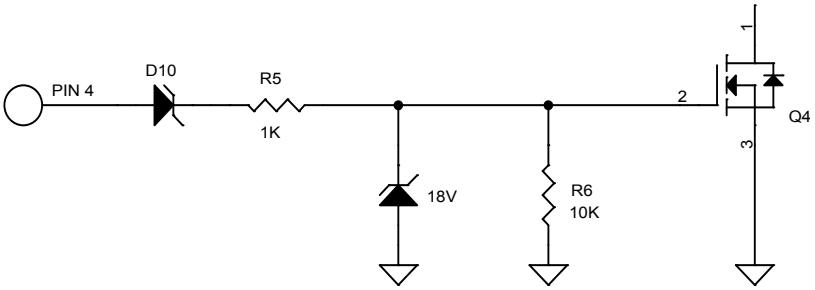
FIG. 2 PROGRAM VOLTAGE



0-10V CONTROL R66,67 = 10K

0-5V CONTROL R66,67 = 20K

FIG. 3 INHIBIT



4813C.DSN

DWG#: 5077T
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5723/5724/5753/5754 INTERFACE CIRCUITS
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FIG. 4 END OF CHARGE

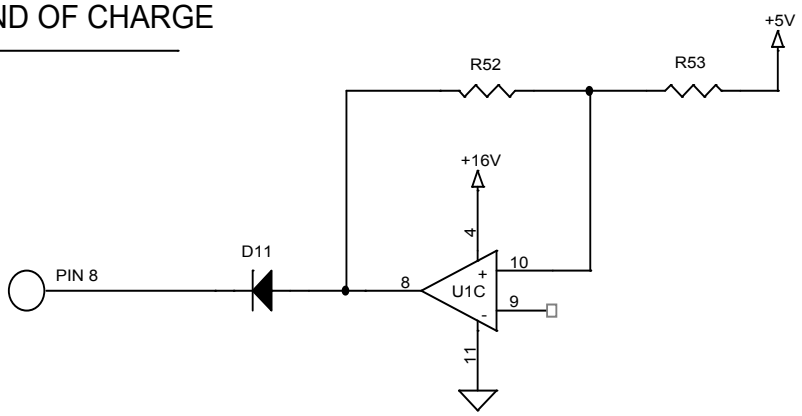


FIG. 5 +5V REFERENCE

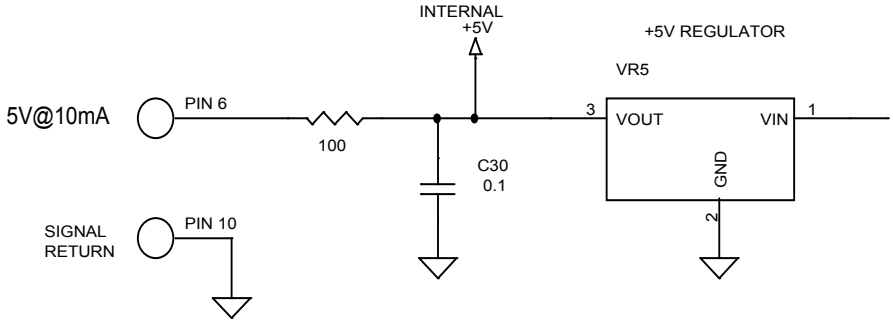
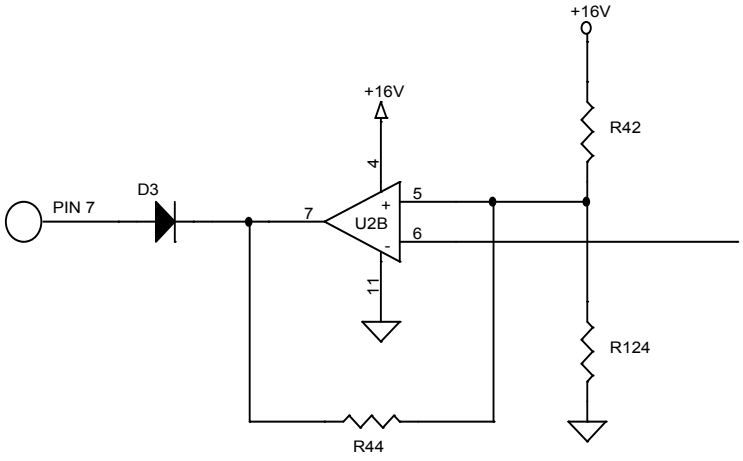


FIG. 6 OVERTEMP STATUS



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The following connections are available when the Optional I/O (-2) Interface is chosen:

PIN	Signal Name	Description
1	INHIBIT	3.5 to 30V input to inhibit charger. 10kΩ load impedance (reference Figure 1).
2	N/C RESERVED	NO CONNECTION.
3	OVERTEMP STATUS IND	Internal 1kΩ resistor to +12V. FET output rated at 40V, 100mA. Fault indicated by low condition (reference Figure 2). This is a warning indicator only.
4	PROGRAM RTN / GND	Differential input return for program voltage. Requires no connection if unit is configured as single ended or internal control (reference Figure 3).
5	PROGRAM VOLTAGE	0 to 10V control differential input for 0 to 100% rated output voltage (reference Figure 3). Two circuits connected in parallel.
6	OVERVOLTAGE STATUS IND	Internal 1kΩ resistor to +12V. FET output rated at 40V, 100mA. Maximum output voltage can be programmed by an internal 10-turn potentiometer. If this voltage is exceeded a fault will be indicated by a low signal at this pin. The charger will also be inhibited to prevent it from exceeding this voltage. The over-voltage protection potentiometer is accessible at the hole labeled OVP Adjust. Counter-clockwise motion of this potentiometer is required to increase the over-voltage set point (reference Figure 4).
7	VOUT PEAK HOLD	Output monitors output voltage with a peak-hold circuit. 0 to 10V represents 0 to 100% rated output voltage. To ensure good stability, the time constant of this circuit is . 2 min. This should be considered when lowering the operating voltage from a higher value. For a direct reading of the output voltage, pin 8 can be monitored (reference Figure 5).

8	Vout MONITOR	Output which directly monitors output voltage. 0 to 10V represents 0 to 100% rated output voltage (reference Figure 6).
9	+12VDC	12V output capable of delivering 30mA (reference Figure 7).
10	N/C RESERVED	NO CONNECTION.
11	+10V REFERENCE	10V output capable of delivering 2mA (reference Figure 8).
12	SIGNAL RETURN	Signal return for any external control circuitry. Common to pin 14.
13	END OF CHARGE IND	Internal 1kΩ resistor to +12V. FET output rated at 40V, 100mA. When PFN is charged to programmed voltage the output is pulled low (reference Figure 9).
14	SIGNAL RETURN	Signal return for any external control circuitry. Common to pin 12.
15	GROUND INTERLOCK	Must be connected to signal return pin or charger will remain inhibited (reference Figure 10)

5723/5724/5753/5754 INTERFACE CIRCUITS
(OPTIONAL INTERFACE)

FIG. 1 INHIBIT

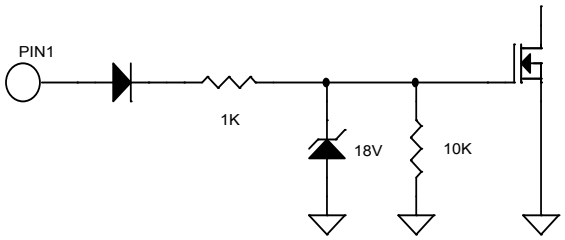


FIG. 2 OVER TEMP STATUS

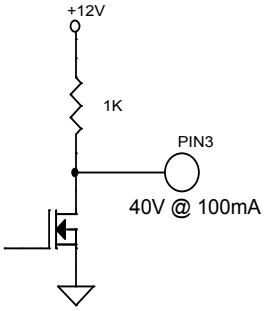
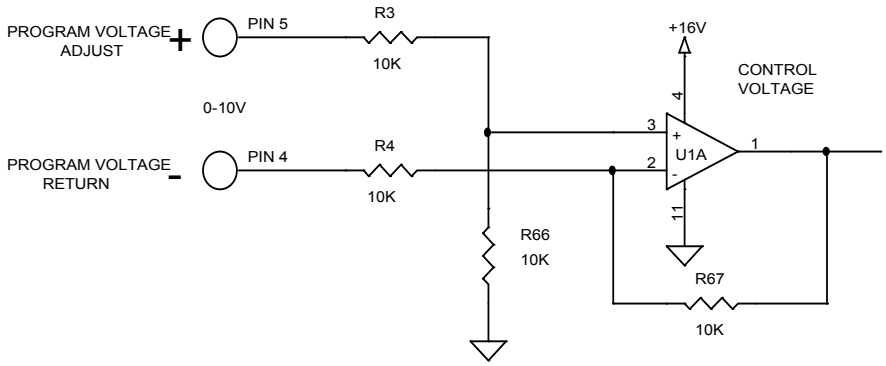
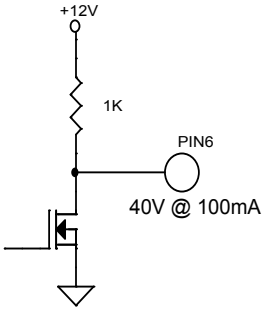


FIG. 3 PROGRAM VOLTAGE



0-10V CONTROL R66,67 = 10K
0-5V CONTROL R66,67 = 20K

FIG. 4 OVER VOLTAGE STATUS



4813C.DSN

5723/5724/5753/5754 INTERFACE CIRCUITS
(OPTIONAL INTERFACE)

FIG. 5 VOLTAGE PEAKHOLD

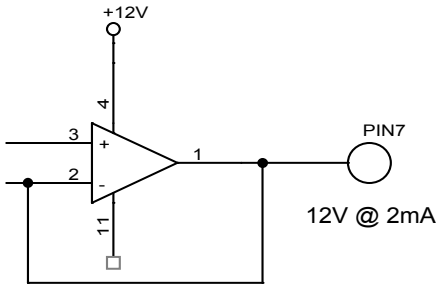


FIG. 6 VOLTAGE MONITOR

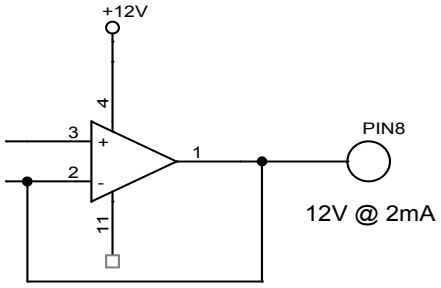
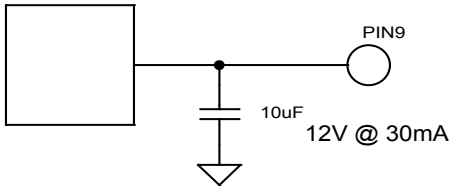


FIG.7 +12VDC



4813C.DSN

5723/5724/5753/5754 INTERFACE CIRCUITS
(OPTIONAL INTERFACE)

FIG. 8 +10VDC REFERENCE

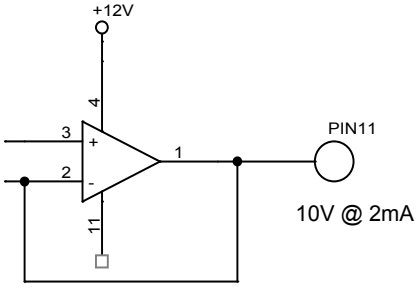


FIG.9 END OF CHARGE

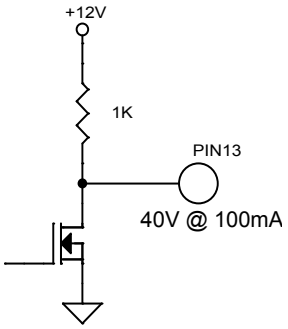
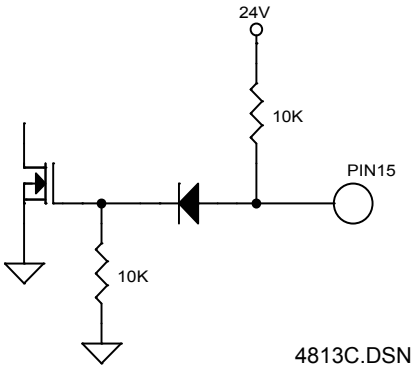


FIG.10 GND INTERLOCK



SECTION 4

OPERATION

4.0 OPERATION

4.1 Power Up Sequence

Care must be exercised in the power up sequence. This is especially true with a microprocessor-controlled system. The proper power up sequence is as follows:

All external control circuitry should be powered up and stable before applying AC to the power module. Inhibit should be high and Program voltage should be 0V. Once these conditions are true, the AC mains power input may be applied.

4.2 Power Down Sequence

Inhibit should be raised high and program voltage should be set to 0V. The AC mains power may then be removed.

4.3 Cooling

Adequate cooling must be maintained at all times the power module has power applied to it. An inadequate airflow will result in the temporary shutdown of the power module.

Ducting should be fabricated to ensure the maximum airflow through the power module. The end away from the face with the AC IN connector, should be oriented toward the cooler air B air exits from the end with the AC IN connector on it.

SECTION 5

MAINTENANCE

5.0 MAINTENANCE

No maintenance is required.

CAUTION

To prevent electric shock, do not remove screws. There are no user serviceable parts inside. Refer all servicing to factory qualified service personnel.

SECTION 6

DOCUMENTATION

6.0 DOCUMENTATION

Temperature Test Point Data

Power Declination Curves

Declaration of Conformity

International Representatives

DWG#: 5077T
REV: E
FILE#: 5077te.doc
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DCN#: 3027

TEMPERATURE TEST POINT DATA

TEMPERATURE IN CENTIGRADE	TEST POINT VOLTAGE	TEMPERATURE IN CENTIGRADE	TEST POINT VOLTAGE
25E	5.28V	50E	7.23V
26E	5.38V	51E	7.29V
27E	5.48V	52E	7.34V
28E	5.57V	53E	7.39V
29E	5.66V	54E	7.44V
30E	5.76V	55E	7.49V
31E	5.85V	56E	7.55V
32E	5.94V	57E	7.58V
33E	6.03V	58E	7.62V
34E	6.11V	59E	7.67V
35E	6.20V	60E	7.71V
36E	6.29V	61E	7.75V
37E	6.36V	62E	7.79V
38E	6.44V	63E	7.82V
39E	6.51V	64E	7.85V
40E	6.59V	65E	7.89V
41E	6.67V	66E	7.92V
42E	6.74V	67E	7.95V
43E	6.81V	68E	7.99V
44E	6.88V	69E	8.02V
45E	6.94V	70E	8.05V
46E	7.00V	71E	8.07V
47E	7.06V	72E	8.10V
48E	7.12V	73E	8.13V
49E	7.17V	74E	8.15V

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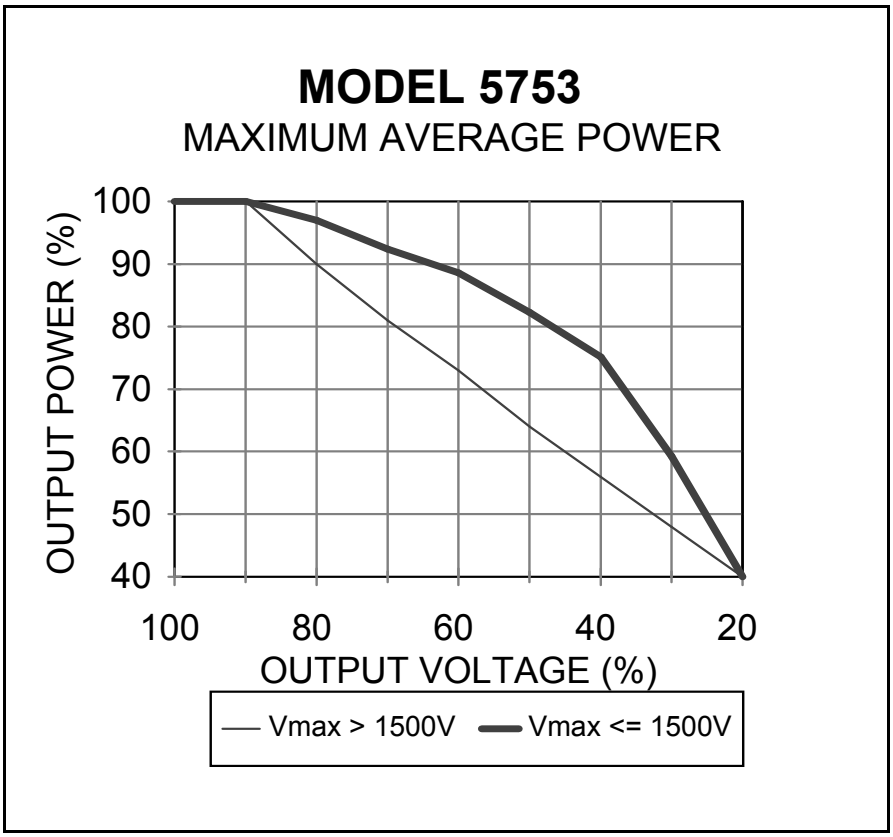

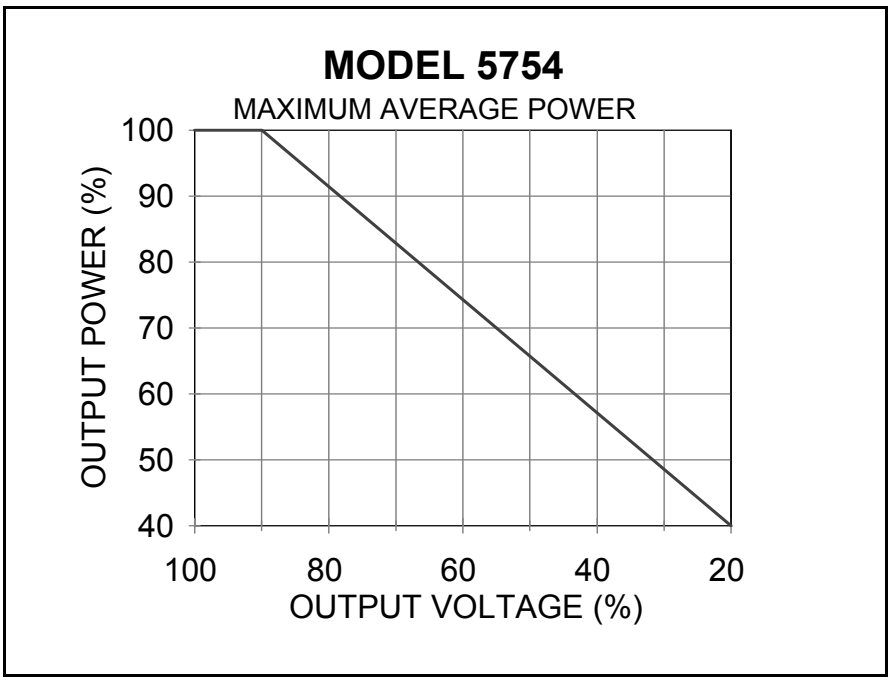

INTERNATIONAL REPRESENTATIVES

Please see Analog Modules, Inc.'s Web Site for a list of International Representatives.

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