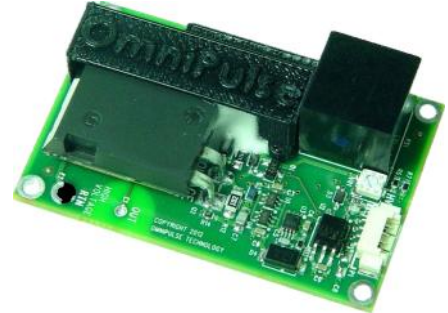


Analog Modules, Inc.

POCKELS CELL DRIVER

- 2 to 3.2kV peak voltage
- >500ns pulsewidth (capacitive load)
- Rise-time <20ns into 30pF, 15ns typical
- Single-shot to 30Hz
- Less than 2" square and only 20 grams
- 25ns propagation delay (typ.)
- 12 volt input



The PCD-3-20 is a compact, all solid-state, high voltage Pockels Cell driver. The rise time is <20ns and the voltage can be adjusted with an on-board potentiometer from 2.0 to 3.2kV. An opto-coupler is used on the input trigger signal to provide for galvanic isolation and to eliminate ground loops.

The driver can survive a shorted output and it is stable into any load.

The PCD-3-20 has an insertion delay of 20ns (typical). Novel circuitry eliminates the need for avalanche transistors and external damping resistors.

The output holes can accept either 20AWG wire or a small coaxial cable.

Pulse Output Voltage

Amplitude Range	2 to 3.2kV
Amplitude Adjustment	On-board potentiometer
Pulse Transition Time	<20ns into 30pF, 15ns typical
Pulse Width	>500ns (capacitive load)
Pulse Recurrence Frequency Range	Single Shot to 30 Hz
Load	<30pF. Can survive shorted output. Stable into any load.
Propagation delay	20ns (typical)

Trigger Requirements

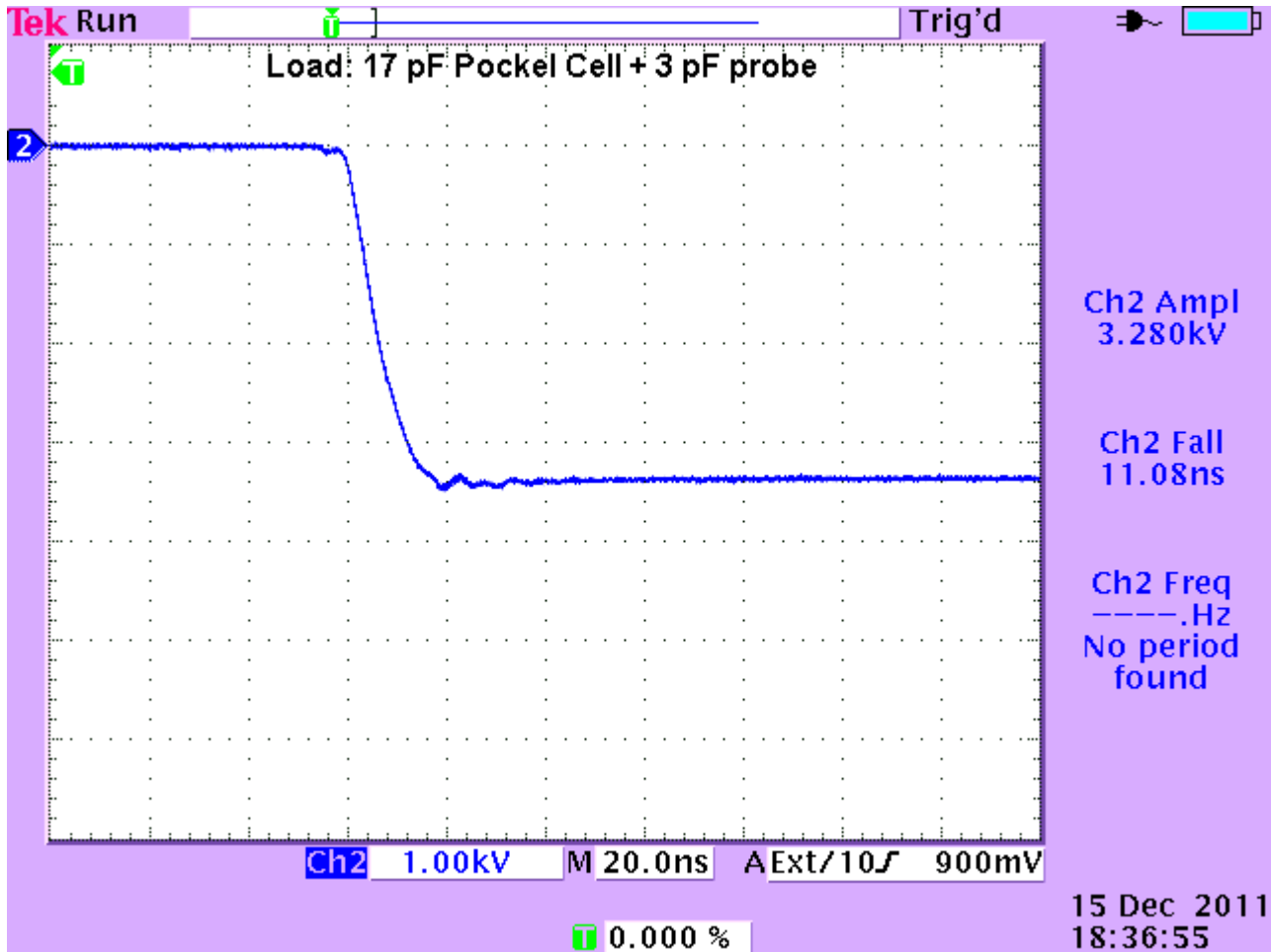
Type	Opto-coupled, 10-15 mA (+5 V), 20ns minimum pulse width
General	
Input Voltage	12 VDC +/- 0.5 V
Input Current	30 to 50mA at 20Hz, 25mA idle
Dimensions (H x W x L) inches	0.7"x1.3"x2.0"

**Specifications are subject to change without notice.*



APPLICATIONS: Rangefinding, remote sensing, research, and other defense and security applications.

In the event this commodity will be transferred to a "foreign person" as defined in 22 CFR 120.16, either outside or within the United States, a validated US State Department license is required.



Output voltage into 20 pF Load
1 kV/div, 20 ns/div

