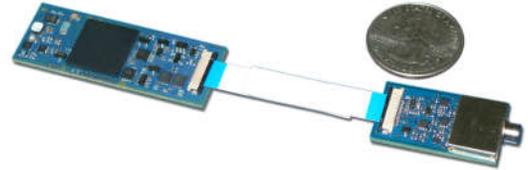


**MINIATURIZED LASER RANGEFINDER RECEIVER
WITH RANGE PROCESSOR**

- HIGH SENSITIVITY DOWN TO 5nW
- AVAILABLE WITH PIN OR APD DETECTOR
- HIGH SPEED RANGE COUNTER
- DIGITALLY ADJUSTABLE APD BIAS, RANGE GATES and FALSE ALARM RATE
- DUAL RANGE TIME PROGRAMMED GAIN
- BUILT-IN HV BIAS SUPPLY FOR APD DETECTOR

**DESCRIPTION:**

The **7551** Laser Rangefinder Receiver/Processor assembly comprises a high performance InGaAs receiver (PIN or APD) with compact support electronics to provide corrected range data to a host system. The serial range data output is calibrated using AMI's patented technology to mitigate the effects of range errors from various sources including walk due to return signal amplitude variations. Exceptional sensitivity allows the use of low power lasers, or alternatively, long range operation. The module provides high voltage bias, time programmed gain, first, last or strongest pulse selection, range gating as well as a number of user-controlled options via the serial interface. The compact layout allows positioning at the optics for minimum overall system size.

SPECIFICATIONS:**Detectors/Sensitivity**

(50% Pd; 20ns pulse; 1550nm; 0.1% FAR)
Detector type: InGaAs PIN (-02), APD (-04)

MODEL	DET DIA	TYP	MAX
7551-02	300um	25nW	45nW
7551-04	200um	5nW	8nW

Multiple Target Resolution

1:1 High Gain 25 meters typical
1:1 Low Gain 15 meters typical

Dynamic Range 10⁶:1
Min/Max Range 50m to 65km

Range Accuracy <1m RMS typical, 2m RMS max. – digitally corrects for fixed-threshold receiver range walk as a function of signal amplitude (single pulse)

Range Gate Digitally adjustable via serial interface

FAR False alarm rate adjustable via serial interface

APD Bias Detector bias adjustable via serial interface

Range Logic First, last or strongest pulse range logic selection via serial interface

TPG Precursor sets receiver in low gain until start pulse detected, after which the receiver time programmed gain is initiated

Start Pulse Electrically or optically injected

Serial I/O LVTTTL

Power +5V @ <70mA (pk), 3.3V @ <75mA (pk)
15mW avg at 5 ranges per second

Temperature Operating -40° to +71°C
Storage -40° to +85°C

Size 1.14" x 0.64" Receiver PCB
2.00" x 0.70" Processor PCB

Weight <7.7g



In the event this commodity will be transferred to a "foreign person" as defined in 22 CFR 120.16, either outside or within the United States, a validated US State Department license is required.

U.S. Patent No.8,619,239

PRELIMINARY

Model 7551 Outline Drawing

