

OEM Seed Laser Diode Driver Assembly

- OUTPUT CURRENT UP TO 2.5 AMPS
- OUTPUT PULSEWIDTH 20 ns TO CW
- COMPLIANCE VOLTAGE TO 10.0 V
- ON-BOARD PULSE GENERATOR
- DIGITAL OR ANALOG CONTROL
- +5 VDC INPUT POWER
- RoHS COMPLIANT



DESCRIPTION:

AMI's Model 762 OEM programmable seed laser diode drivers are ideal for driving 14-pin butterfly packaged laser diode modules for use in CW or pulsed fiber MOPA systems. Applications include materials processing, LIDAR systems for remote sensing, laser communication and rangefinding.

SPECIFICATION:

		762			762-EXT		
PARAMETER	Min.	Typical	Max.	Min.	Typical	Max.	Units
INPUT							
Power (Driver and TEC)	4.75	5.0	5.25	4.75	5.0	5.25	VDC
Current (Driver and TEC)	-	0.330	3.5	-	0.330	3.5	А
Power (Laser)	3.0	5.0	12	3.0	5.0	12	VDC
Current (Laser) (Laser dependent)	-	-	2.5	-	-	2.5	А
Control Voltage (50 Ω Impedance)	-	N/A	-	0	-	2.5	V
OUTPUT							
Current	0.1	-	2.5	0.1	-	2.5	А
Compliance Voltage	-	2.0	10.0	-	2.0	10.0	V
Pulsewidth	20	-	CW	20	-	CW	ns
Repetition Rate	Single Shot	-	10	Single Shot	-	10	MHz
Duty Cycle	0	-	100	0	-	100	%
Risetime (Optical) @ 2 A	-	10	15	-	10	15	ns
Falltime (Optical) @ 2 A	-	10	-	-	10	-	ns
Analog Back Facet Monitor	0	-	3.7	0	-	3.7	V
TEC Voltage	0	-	4.2	0	-	4.2	V
TEC Current	0	-	3.0	0	-	3.0	А

*Faster risetimes, shorter pulse widths, higher voltage, and CW operation possible under certain operating conditions (consult factory).

Specifications are subject to change without notice.

APPLICATIONS:

Seeding Fiber Lasers, Remote Fiber Optic Sensing, Laser Communication

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PROTECTION:	Adjustable current limit
	Driver disabled when laser temperature exceeds temp window
CONNECTIONS:	
Back Facet Monitor:	Micro Coax Connector (Amphenol 908-24100)
Power:	3 pin Terminal Block (<i>Molex 0</i> 393570003)
Interface:	8 & 14 Pin AMP MicroMatch Connectors (7-215460-8 & 8-215460-4)
TEMPERATURE:	
Operating:	0°C to +50°C
Storage:	-20°C to +70°C
SIZE:	2.9" x 3.00" x 0.5"
DIGITAL CONTROL:	Asynchronous (9600bps, 8nl serial) protocol or I ² C slave protocol (100 or 400 kHz). All logic inputs are TTL, 5 V CMOS compatible. Digital outputs are pulled up to +5 V internally with 4.75 k Ω except AMP SYNC1/AMP SYNC 2 pulled up to +5 V with 300 Ω .
THERMAL:	On-board TEC Controller will provide heating and cooling as necessary to maintain desired operating point. Thermistor and the TE cooler are in the laser diode package (not included). Customer may need to provide thermal mass for heatsinking under high dissipation conditions.

OPERATING NOTES:

The driver circuitry operates from a single +5 V power source. Additional voltages are generated on the board by high efficiency switching power supplies. The laser power input may be operated down to +3 V to conserve power at the expense of switching speed. Input laser power may also be increased up to +12 V to enhance switching speed for high inductance lasers. For most applications laser power may be tied to the driver +5 V supply, or through an external switch as an additional safety interlock.

An on-board field programmable gate array (FPGA) is programmed to handle communications, to modify adjustable features, and to provide external flags and signals to a host system. The FPGA also contains a comprehensive pulse generation system with many programmable features. Adjustments can also be made through analog operation by the use of multi-turn potentiometers and providing an external TTL trigger pulse so a serial interface is not required. A graphical user interface (GUI) program is included for easy control and programming from a PC. The driver supplies a bidirectional proportional-integral-derivative (PID) thermoelectric cooler (TEC) controller with current capability of 3 A and voltage capability of 4.2 V.

The 762-EXT is offered for those who require agile control of the laser current, want to modulate the current or drive the laser with arbitrary waveforms, pulses with variable rise/fall times or modified pulse flatness. An external control voltage with a calibration of 1 A/V is required for this mode of operation and the potentiometer and digital control of the laser current are disabled.

The board is manufactured as a RoHS compliant assembly built to the Directive 2002/95/EC requirements. A heatsink adapter plate to mount to an external heatsink and all required mating cables are supplied with each unit. Contact AMI today to discuss your custom requirements.

14-pin Connector (J7)

Pin	Signal
1	ENABLE
2	GND
3	CONTROL VOLTAGE
4	MONITOR GAIN MSB
5	MONITOR GAIN LSB
6	GND
7	TRIG/PW
8	GND
9	TRIG/PW
10	GND
11	I ² C CLK/ASSYNC TX
12	I ² C DATA/ASSYNC RX
13	GND
14	SERIAL SEL.

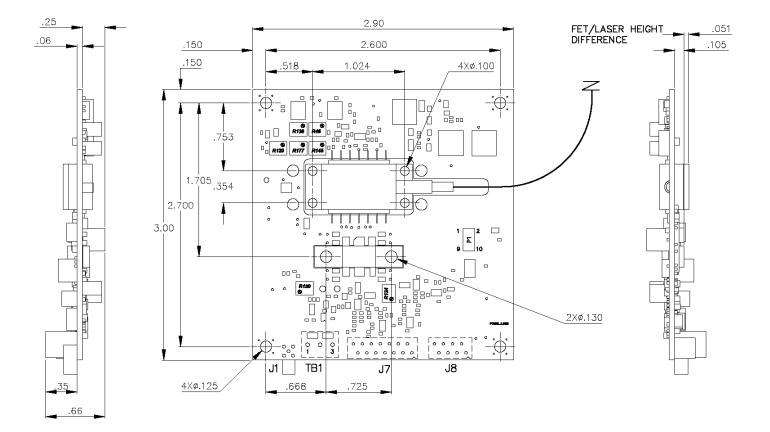
8-pin Connector (J8)

Pin	Signal
1	CURRENT FAULT
2	TEMP. FAULT
3	GND
4	LASER FIRE OUT
5	GND
6	AMP SYNC1
7	GND
8	AMP SYNC2

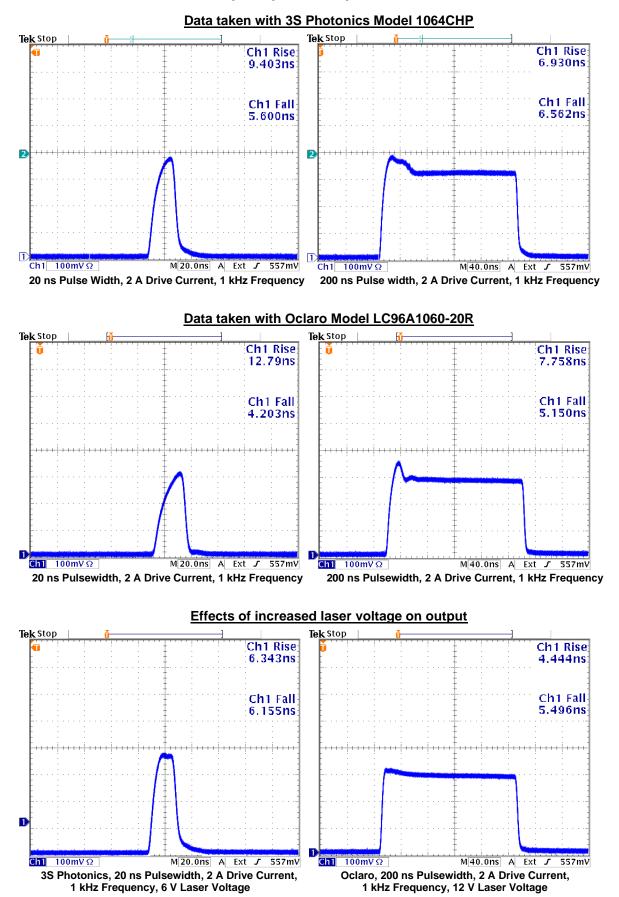
14-Pin Butterfly Package Interface (Customer-supplied)

(Customer-supplied)				
Connection				
TEC Cooler (+)				
Thermistor				
Back facet monitor anode (+)				
Back facet monitor cathode (-)				
Thermistor				
N/C				
Laser diode anode (+)				
Laser diode cathode (-)				
N/C				
Case ground				
TEC cooler (-)				





Sample Optical Output Waveforms



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Included Graphical User Interface Program

💀 762 / 762-EXT - Seed Laser Diode Driver - 77	07 - Revision B					
Communication Options Adjustable Parameters - Digital to Analog Converter Values Status						
9600-8-N-1 COM1 Open Async.	EN Min Value Max Description	762 Enable (Low)				
12C Address: 0x50 400 kHz 🗸 Open 12C	1 ≤ ≤ Set Laser Fire Threshold (mA)	762 Ready				
762 Device Information (12C Address	2	DAC Ready				
Connected: No	3	EEPROM Ready				
Serial Num: Not Connected	4	Temp Fault (Low)				
Signature: Not Connected	5	Current Fault (Low)				
Memory Control	6	TEC Shutdown				
Load from EEPROM Write SRAM Bank Num.	7 □ ≤ Set Laser Diode Bias Curr. (mA)	762 Error				
Save to EEPROM Read SRAM 1	8 ≤ Set Test Point E7 - NC (V)	Int. Memory Error DAC Error				
Enable	Enable All DACs Set All All DACs (Simultaneously)	EEPROM Error				
Ext. HW Control O Int. Mem. Control Set	Show Raw Values					
Enable O Disable Set	Trigger Configuration and Options	Bank Number				
Serial Select	O Follow External Trigger PW: 078 マ x10ns. Set	Get Status				
Ext. HW Control O Int. Mem. Control Set	Use External Trigger Delay: x10ns. Set	Base				
12C Asynchronous Set	Use Ext. Trigger as Gate Use Ext. Trigger as Sync Trigger Period: x10ns. Set	Hexadecimal (F5)				
Monitor Gain	Int. Pulse Generator Set	Decimal (F6)				
Ext. HW Control O Int. Mem. Control Set						
◯ 11 ◯ 10 ◯ 01 ◯ 00 Set	Amp. Sync 1 Options Pulsewidth: 0/8 v10ns. Set Pulsewidth:	0/8 🗸 x10ns. Set				
Manual Command Pulse Enable						
1 2 3 Trigger / PW	Set Delay: x10ns. Set Set Delay:	x10ns. Set				
Send Clear Amp. Sync 1	TEC Shutdown					
1: Command (W, R, L, S, T, B) 2: Command Dependent	Always Operating O Int. Mem. Control					
3: Command Dependent	Operating O Shutdown Set	Build 1.2.2.36				